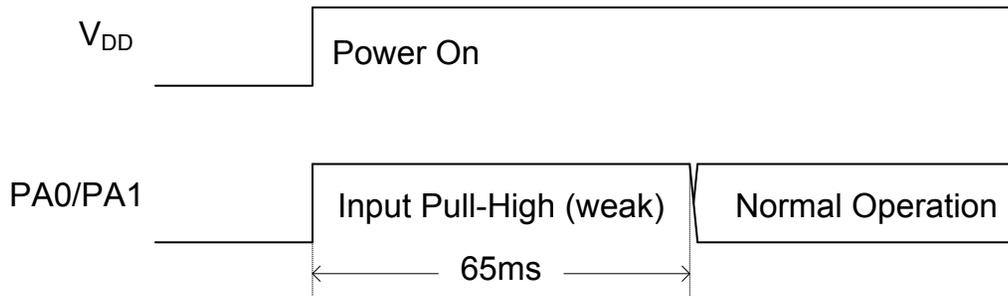# Notices for Powered-on Pins of NY5PxxxJ OTP Series

Description: When NY5P025J/055J/085J/185J/345J/520J/720J is powered on, the PA0 and PA1 pin will be kept in the input pull-high(weak) status for about 65ms. Please pay attention to the design plan of PA0 and PA1 pin.

Reason: When IC is powered on, the PA0 and PA1 pin will be kept in the input pull-high(weak) status for about 65ms. As Shown below.

Solution:

1. It's recommended to set PA0 and PA1 as inputs to avoid pull-high affects external circuits when power is on.
2. If the PA0 or PA1 is set as output pin, please confirm that the pull-high won't cause malfunction to external circuit.

   For examples, use sink connection for LED applications, or use PNP transistors for BJT driver motor applications.